

Docket: T00-338  
S/N 09/785,114



To: Commissioner of Patents and Trademarks  
Washington, D.C. 20231

From: George O. Saile, Reg. No. 19,572  
28 Davis Ave.  
Poughkeepsie, NY 12603

Subject:

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TECHNOLOGY CENTER 2800

#9/B  
2/28/03  
Jum.  
(N.E.)

Docket T00-338  
S/N 09/785,114  
File date: 2/20/2001  
Inventor: Chin

Title : **A LOW LEAKAGE ONE TRANSISTOR STATIC  
RANDOM ACCESS MEMORY**

Group Art Unit: 2823  
Examiner: Berezny

OK to enter  
2/18  
14 APR 03

### RESPONSE TO PATENT OFFICE ACTION

Dear Sir:

In response to the office action dated 12/03/02, please consider the following  
remarks:

### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patent and Trademarks, Washington, D.C. 20231, on February 3, 2003.

Signature/Date

 2/3/03  
Stephen B. Ackerman, Reg. #37,761

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**Charge to Deposit Account**

The Commissioner is hereby authorized to charge payment of the fee of \$ 0 associated with this communication, or credit any overpayment, to Deposit Account No. 19-0033. The Commissioner is also authorized to charge any additional fee under 37 CFR §1.16 and 1.17 to this Deposit Account. A duplicate copy of this sheet is enclosed. The Commissioner is hereby authorized to charge payment of any additional fees involved with added Claims and the like to Deposit Account No. 19-0033.

Respectively submitted,

\_\_\_\_\_  
Date

\_\_\_\_\_  
George O. Saile; or  
Stephen B. Ackerman